

JAN 04 2006

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REPLY UNDER 37 CFR §1.116
EXPEDITED PROCEDURE
TECHNOLOGY CENTER 2800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Syun-Ming Jang, et al.

Serial No.: 10/826,899

Filed: April 16, 2004

For: LOGIC-EMBEDDED MEMORY
INTEGRATED CIRCUITS

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Attorney Docket No.:
2003-1383 / 24061.231

Customer No. 42717

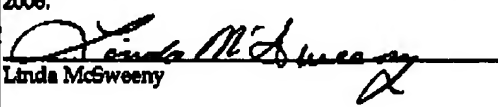
Group Art Unit: 2814

Examiner: Wai-Sing Louis

Confirmation No.: 2231

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Linda McSweeney

Sir:

RESPONSE UNDER 37 C.F.R. §1.116

This Response is submitted in reply to the Office Action mailed on November 14, 2005.
(Please see the pages that follow).